



Micronas.7397

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**APPLICANT:** Czech et al. **GROUP:** 2826  
**SERIAL NO:** 10/761,144 **EXAMINER:** Tan N. Tran  
**FILING DATE:** January 20, 2004  
**FOR:** VOLTAGE COUPLING CIRCUIT FOR AN INTEGRATED CIRCUIT  
[as amended]

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**AMENDMENT**

This amendment is in response to the Official Action dated March 28, 2006. Please  
amend the application as follows:

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